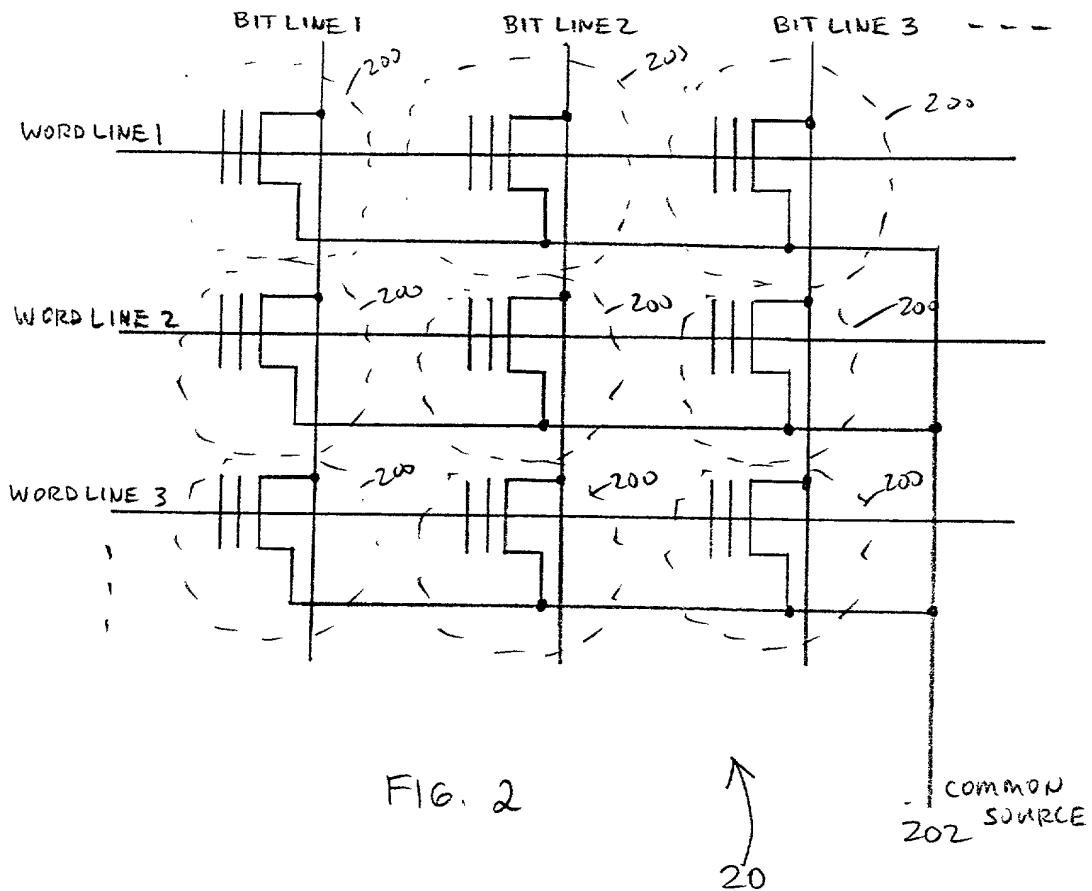


FIG. 1



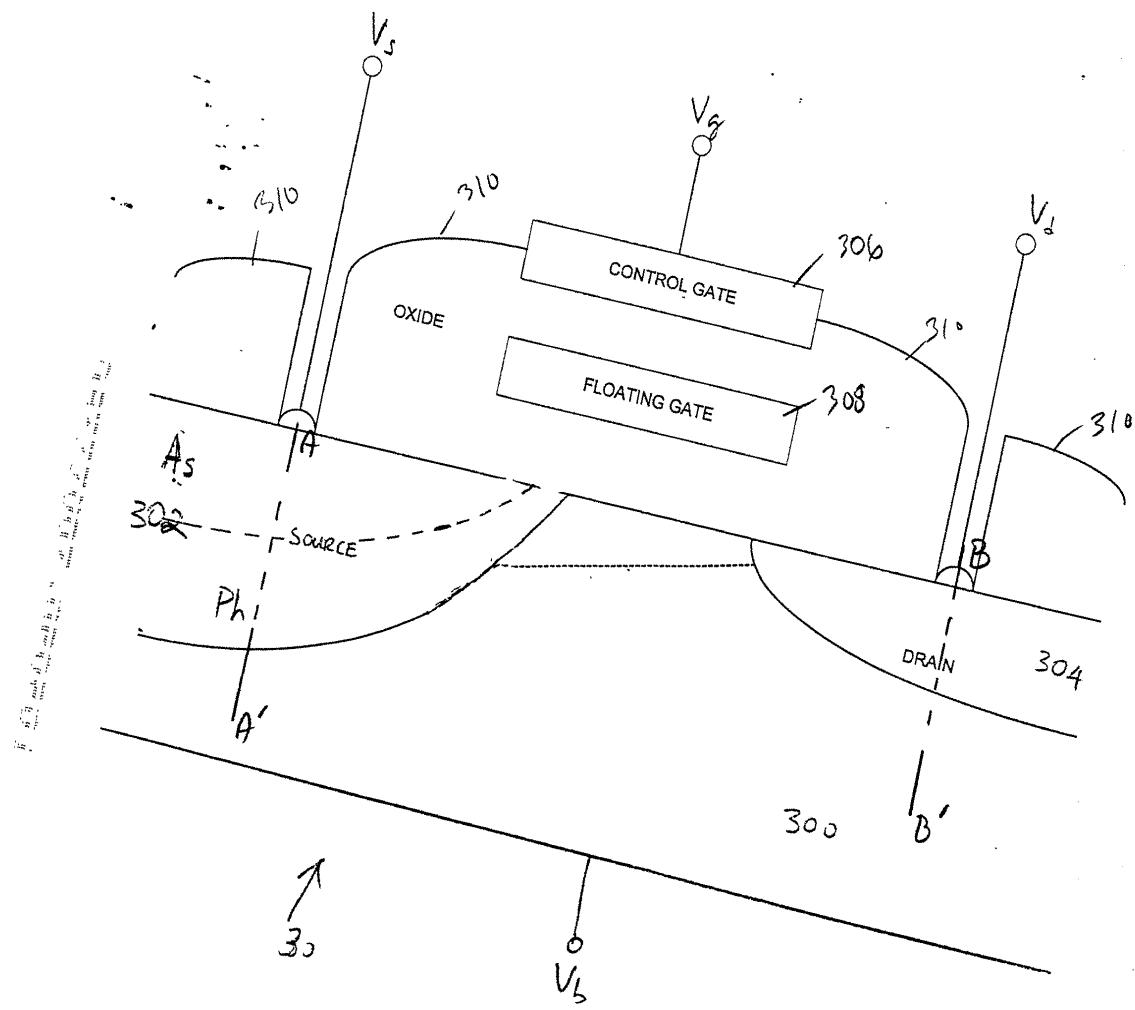
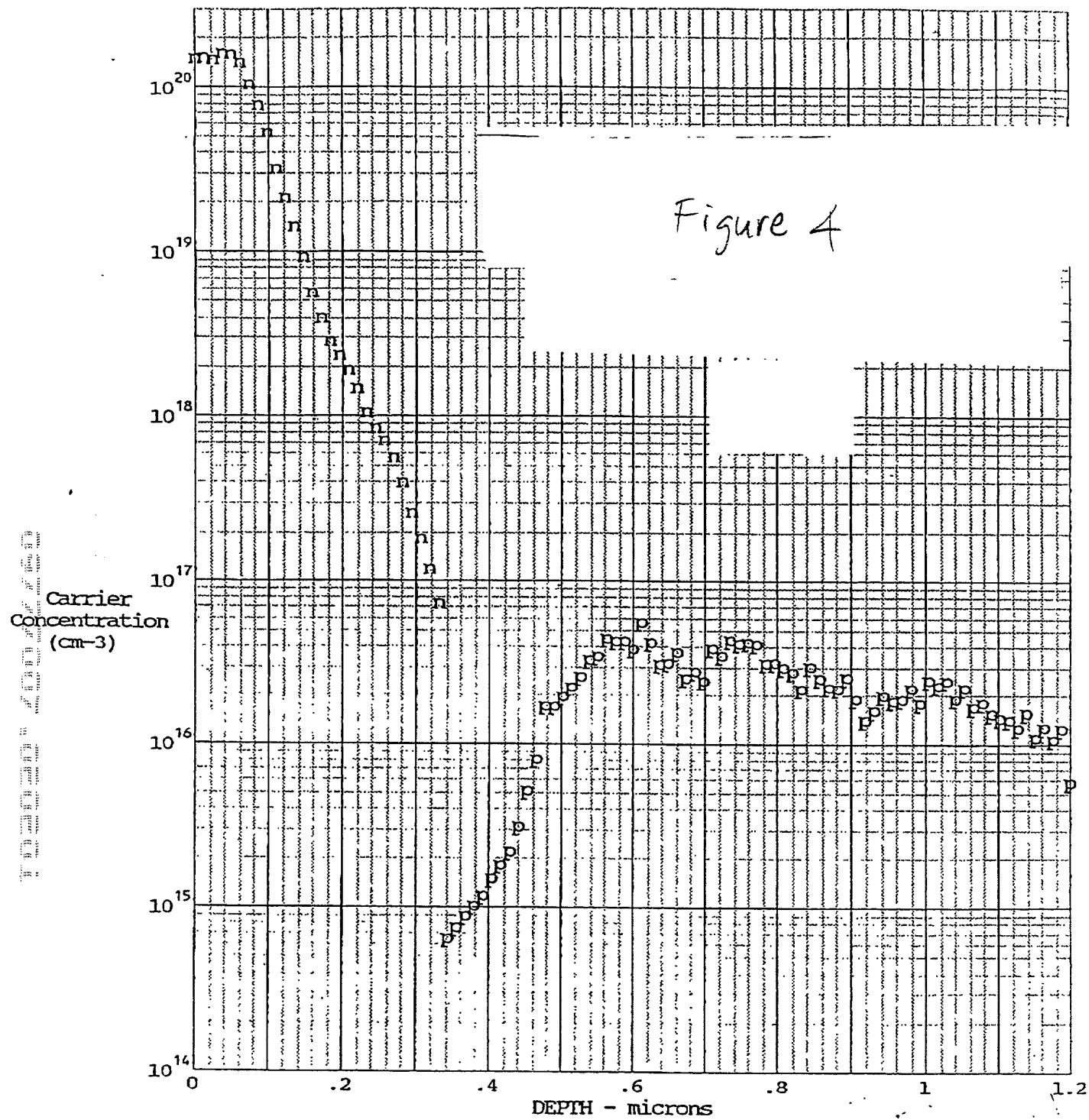
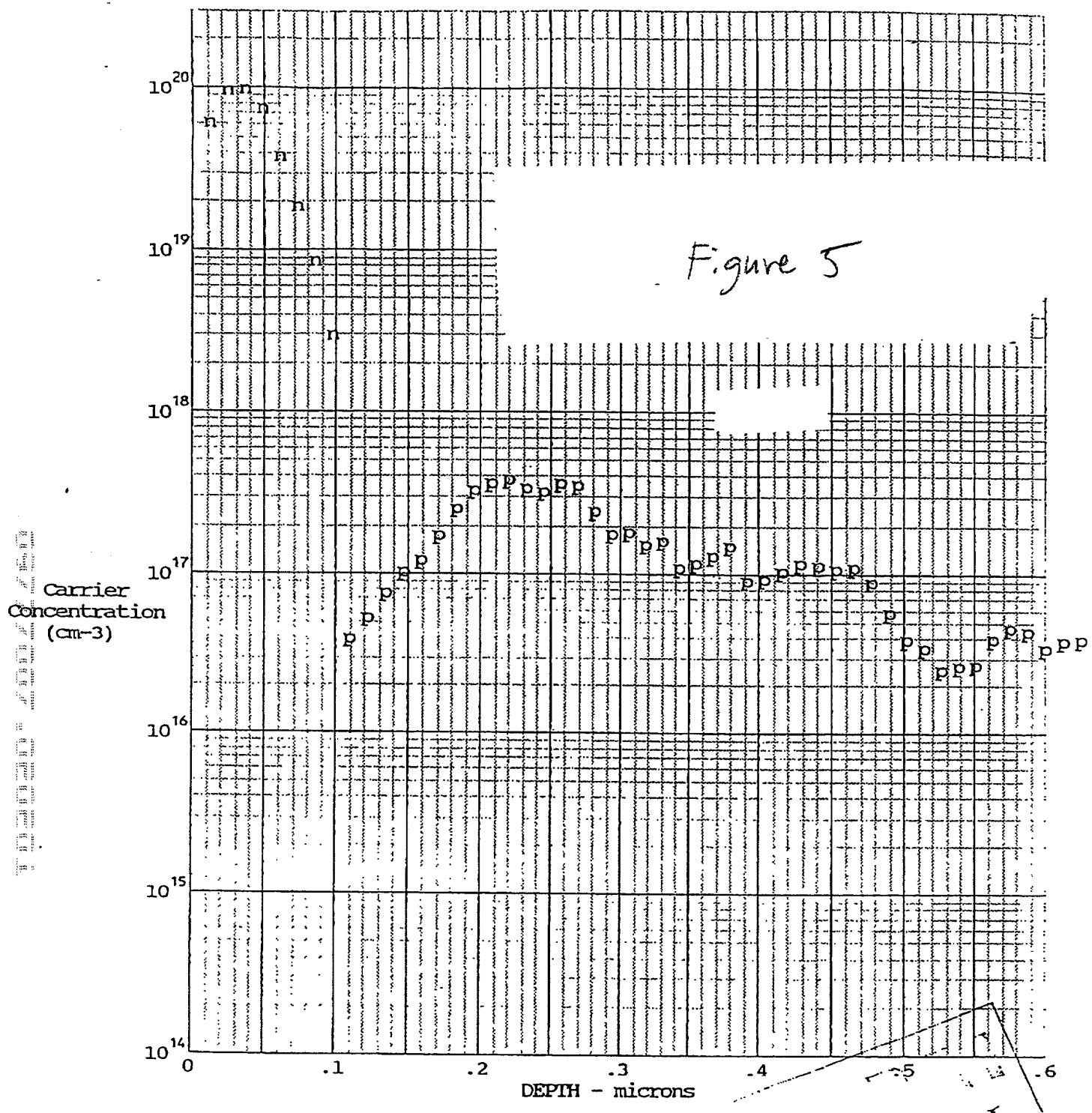


FIG. 3

Figure 4





Drain Side Program Rate Distribution After UV Erase

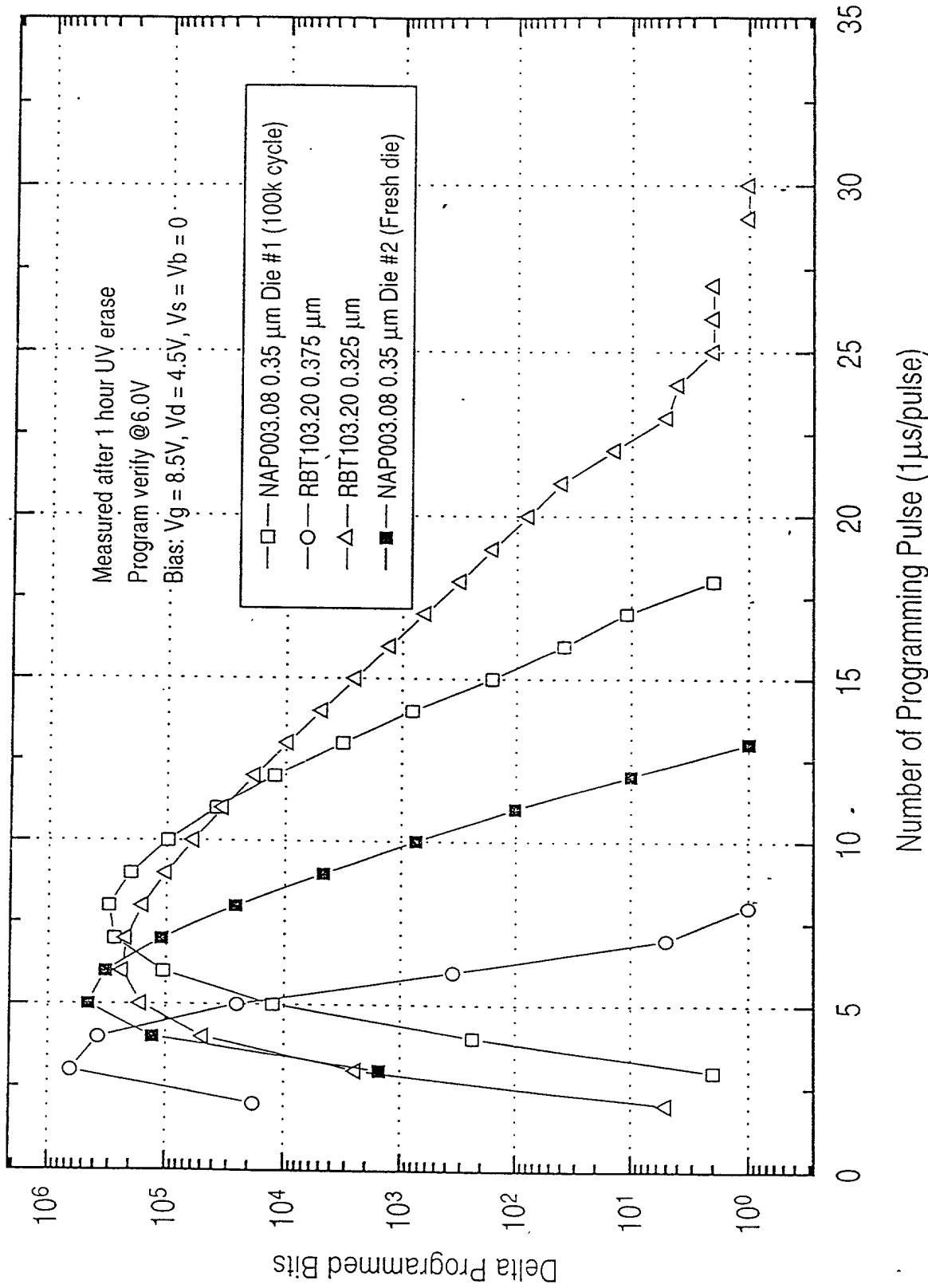


Fig. 16.6

Source Side Program Rate Distribution After UV Erase

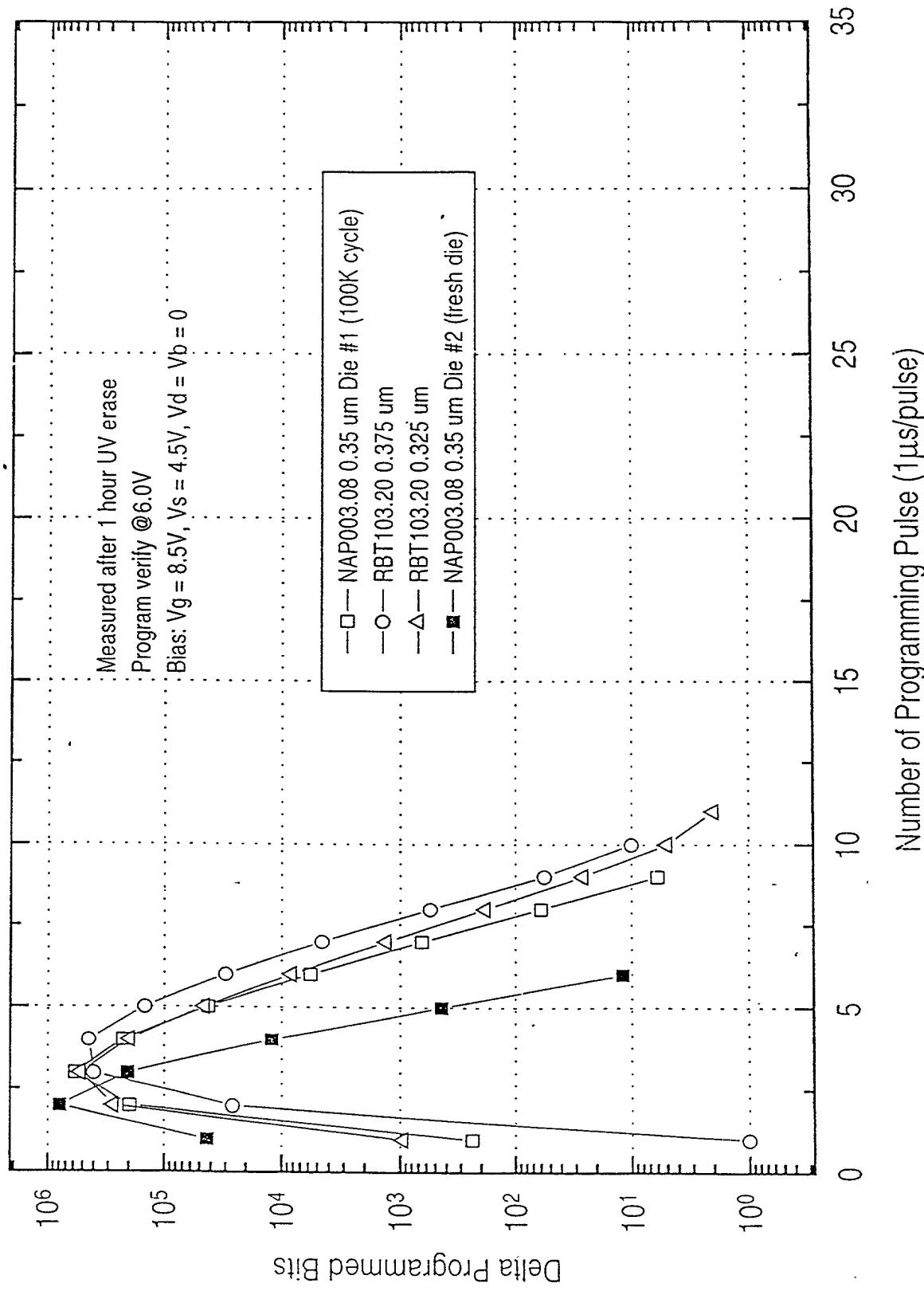


Fig. 7

Gate/Substrate Current Characteristics

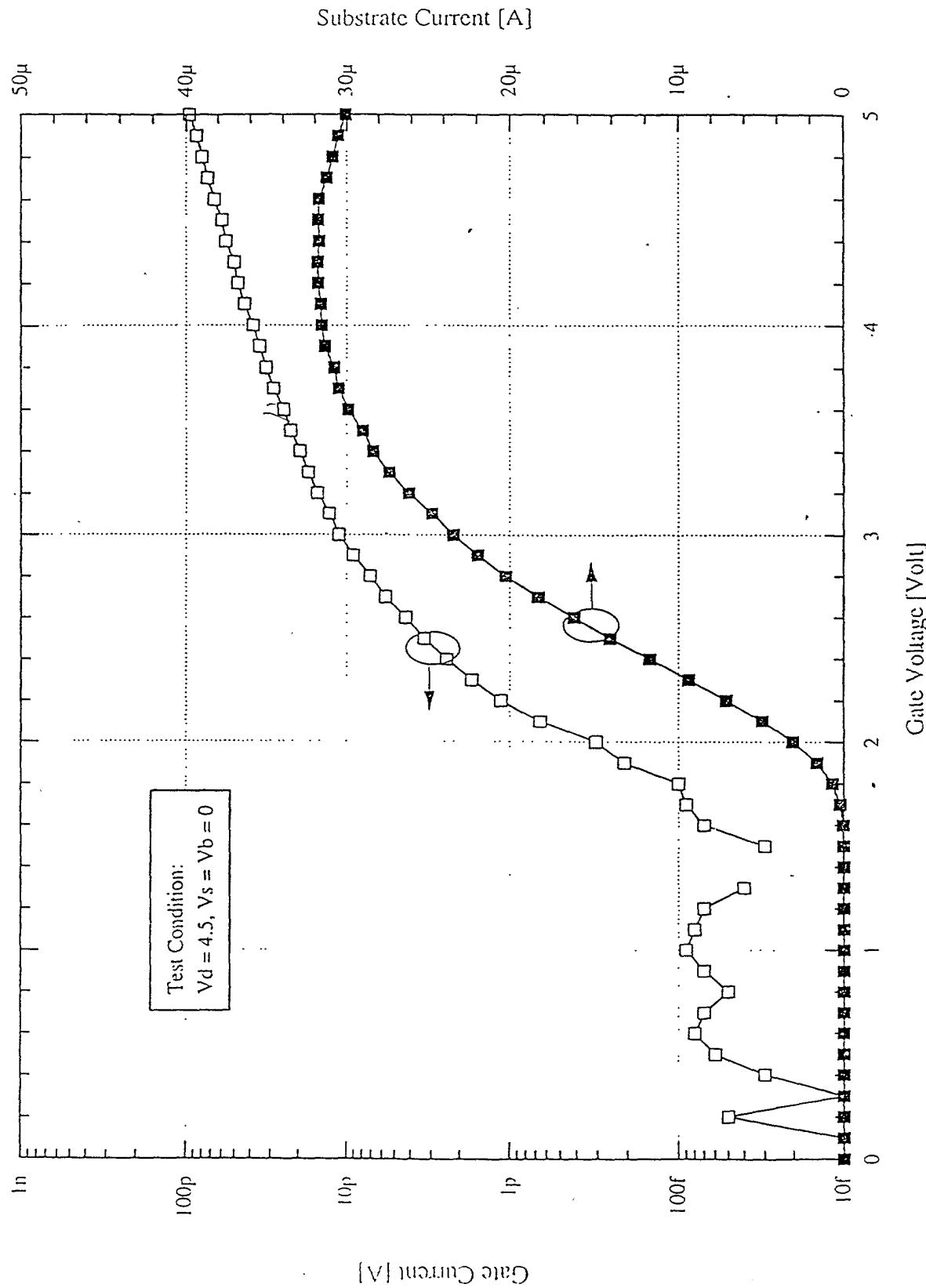


Fig. 8

Gate / Substrate Current Characteristics

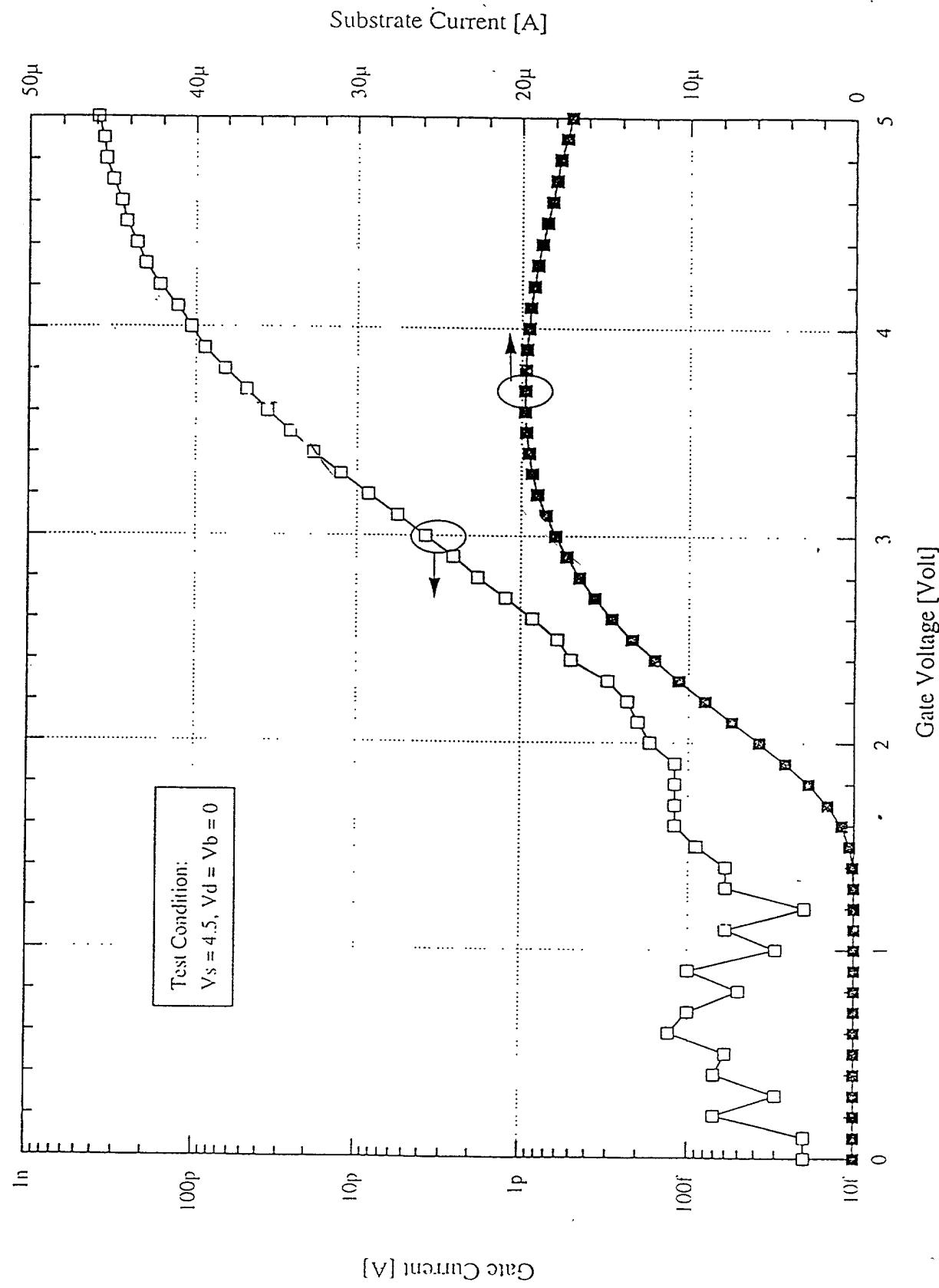


FIG. 9